

HS1A THRU HS1M

High Efficient Rectifier Diode

Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High frequency
- High forward surge capability
- Meets MSL level 1, per J-STD-020,LF maximum peak of 260 °C



Typical Application

For use in high frequency rectification of power supply, inverters, converters, and freewheeling diodes for consumer and telecommunication.



Mechanical Data

- Package: DO-214AC(SMA)
 Molding compound meets UL 94 V-0 flammability rating,RoHS-compliant
- Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102
- Polarity: Cathode line denotes the cathode end

■ Maximum Ratings (Ta=25°C Unless otherwise specified)

DADAMETED	Symbol	Unit	Conditions	HS1							
PARAMETER			Conditions	Α	В	D	F	G	J	K	М
Repetitive Peak Reverse Voltage	V _{RRM}	V		50	100	200	300	400	600	800	1000
Average Forward Current	I F(AV)	Α		1.0							
Surge(Non-repetitive)Forward Current	I _{FSM}	Α	60HZ sine wave, 1 cycle, Ta=25℃	30							
Storage Temperature	T _{stg}	$^{\circ}$		-55 ~ + 150							
Junction Temperature	Tj	$^{\circ}$		-55 ~ +150							

■ Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	Cymhol	Unit	Conditions			HS1						
PARAWETER	Symbol		Conditions		Α	В	D	F	G	J	K	M
Peak Forward Voltage	V _{FM}	V	I F =1.0A		1.0			1.7				
Peak Reverse Current	I _{RRM1}		\/ -\/	Ta=25℃	10							
	I _{RRM2}	μA	$V_{RM}=V_{RRM}$	Ta=125℃	100							
Maximum reverse recovery	_	ns	I F =0.5A I R =1A									
time	Trr		I RR =0.25A		50					75		
Thermal Resistance(Typical)		°C/W	Between junct	75 ¹⁾								
	$R_{\theta J-A}$		ambient									
	R _{0J-L}		Between junction and terminal		27¹)							

Notes:

1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas.

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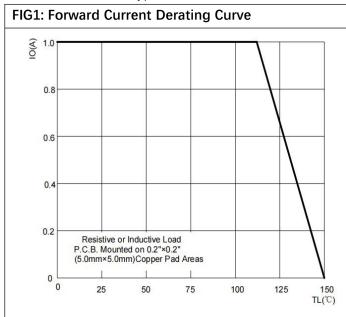






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■ Characteristics (Typical)



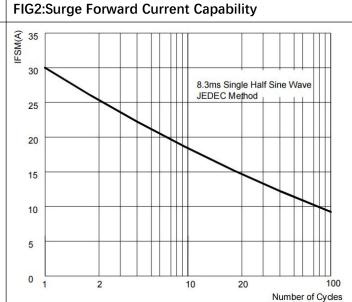


FIG3:Instantaneous Forward Voltage

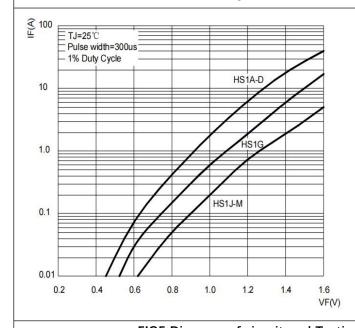


FIG4:Typical Reverse Characteristics

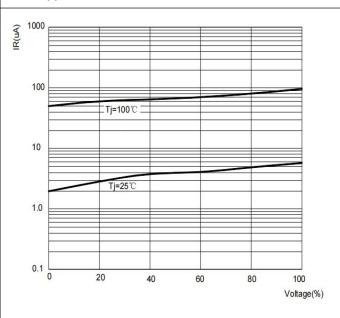
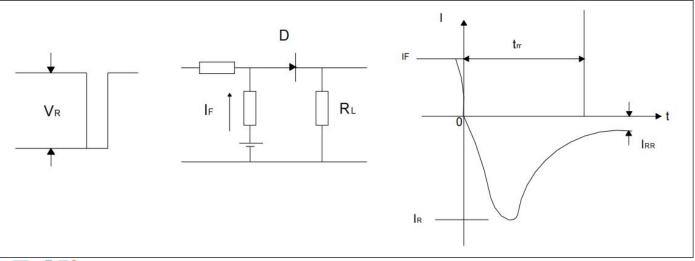


FIG5:Diagram of circuit and Testing wave form of reverse recovery time



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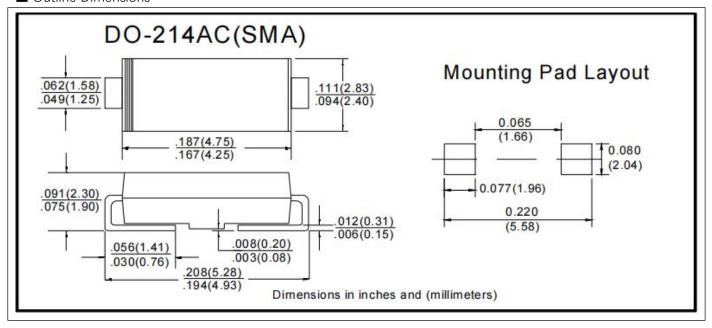


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■ Ordering Information (Example)

PREFERED	PACKAGE CODE			OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
HS1A ~ HS1M	DO-214AC(SMA)	5000	10000	80000	13" reel
HS1A~HS1M	DO-214AC(SMA)	7500	15000	120000	13" reel

■ Outline Dimensions



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